Problem 1. [35 pts]

For the circuits below, please find expressions for the specified voltage or current over the indicated time ranges in terms of the circuit parameters. Plot the waveform on the provided axes, and clearly identify the key parameters in your graph.

(A) [17pts] Consider the circuit of Fig. 1. The switch is closed for t < 0, open for $0 \le t < t_1$, and closed for $t \ge t_1$, where $t_1 = 3L/R$. Find and plot the current $i_L(t)$.



Figure 1





(B) [18 pts] Consider the circuit of Figure 2, in which $\alpha > 0$. The switch is open for t < 0, and closed for $t \ge 0$. $v_c(0) = V_0$. Find and plot the voltage $v_1(t)$.



Figure 2

for t < 0 $V_1(t) = 0$ for t>o consider Thereasin equivalent of the network seen by C: $A_{TEST} = A_{TEST} R_2 + (1+\omega)A_{TEST} R_1$ $A_{TEST} = A_{TEST} R_2 + (1+\omega)A_{TEST} R_1$ $A_{TEST} = R_1 = R_2 + (1+\omega)R_1$ S Dependent source makes R, "look" a factor 1+0. bigger in resistance ? $N_c(0^+) = V_c(0^-) = \overline{V}_o$ $I_{2}(0^{+}) = \frac{V_{0}}{R_{TH}} = \frac{V_{0}}{R_{2} + (1+\alpha)R_{1}} \implies V_{1}(0^{+}) = \frac{(1+\alpha)R_{1}}{(1+\alpha)R_{1} + R_{2}} \cdot V_{0}$ V. (t -> 00) = 0 $T = R_{T\mu}C = \left[(1+\alpha)R_1 + R_2 \right]C$:. $v_1(e) = \begin{cases} 0 & t < 0 \\ \frac{(1+w)R_1}{(1+w)R_1 + R_2} & -\frac{t}{R_1} (1+w)R_1 + R_2] c \end{cases}$ $t \ge 0$ or $V_1(t) = \frac{(1+\alpha)R_1}{(1+\alpha)R_1 + R_2} = \frac{-t}{(81+\alpha)R_1 + R_2}$ where ulto is the unit

sty function .

$$v_{1}(t), t < 0 = O$$

$$v_{1}(t), t \ge 0 = \frac{(1+\alpha)R_{1} - \nabla_{\alpha}}{(1+\alpha)R_{1} + R_{2}} \cdot C$$
Plot $v_{1}(t)$ over all time, indicating important waveform parameters.
$$(1+\alpha)R_{1} + R_{2}$$

$$T = [(1+\alpha)R_{1} + R_{2}]C$$

$$T = [(1+\alpha)R_{1} + R_{2}]C$$

Problem 2 [15 pts] Comparator

In this problem, you are going to work on a comparator circuit which has the input-output relationship as shown in the left figure below, where the switching voltages are $v_{LOW} = 1.5$ V and $v_{HIGH} = 2.5$ V. A possible comparator design for realizing this is given in the right figure below. Note that the supply voltages on the op-amp are 0 and 5V, and that v_{LOW} and v_{HIGH} are not centered around the mid point between 0 and 5 V. R is chosen to be 10 k Ω .



Determine the numerical values of R_1 and V_{ref} needed for reaching the desired $v_{\text{IN}} \sim v_{\text{O}}$ relationship.

Using voltage divider, we determine
$$V_4$$
:
 $V_{+} = V_{ref} + \frac{R}{R_1 + R} (V_0 - V_{ref})$.
In positive feedback Op-Amp $V_0 = 0$ or $5V$.
Switching happens when $V_4 = V_-$
 $V = V_{low} = V_{ref} + \frac{R}{R_1 + R} (0 - V_{ref})$ O
 $V = V_{high} = V_{ref} + \frac{R}{R_1 + R} (5V - V_{ref})$. \textcircled{O} .
 $\textcircled{O} = O$: $V_{high} - V_{low} = \frac{R}{R_1 + R} \times 5V$.
 $\frac{-R}{R_1 + R} \times 5V = 25V - 1.5V = 1V$.

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$$\frac{R}{R_{i}+R} = \frac{1}{5}, \quad R_{i}=4R=40k\Omega$$
Substitute $\frac{R}{R_{i}+R} = \frac{1}{5}$ into $\frac{5}{9}$. $0:$

$$1.5V = V_{ref} + \frac{1}{5}(-V_{ref})$$

$$\frac{4}{5}V_{ref} = 1.5V,$$

$$V_{ref} = 1.875V.$$

$$R_1 = 40k \Omega$$
.
 $V_{ref} = 1.875V$

Problem 3 [20 pts]

(A)[8pts] The schematic below shows a similar circuit as the one used in our Lab 2 for converting digital signal to analog signal, except with a different output port location. Express v₀ as a function of V₁ and V₂.



$$v_{o} = -\frac{1}{6}V_{1} + \frac{1}{6}V_{2}$$

(B) [12pts] The circuit in part (A) is used to drive a load that is a parallel combination of resistance R_L and capacitance C. The switch S1 remains open for a long time before t = 0 so that the circuit is in a stable state. At t = 0, S1 closes. For t > 0, express the load voltage $v_L(t)$.



 $V_{2}(t) = V_{2}(t=\infty) + [V_{2}(t=0) - V_{2}(t=\infty)]e^{-t/z}$ $= \frac{R_{2}}{R_{2} + \frac{2}{3}R} \left(-\frac{1}{5}V_{1} + \frac{1}{5}V_{2} \right) \left(1 - e^{-\frac{1}{2}} \right)$ $\zeta = \frac{\overline{S}RRC}{R_{+}\overline{S}R}$

 $v_L(t) =$

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The inverting amplifier shown in the figure has a parallel combination of a resistor, a capacitor, and an inductor in its negative feedback path. You may assume that the op amp is ideal.

Part A. In the box provided below write a differential equation relating the inductor current $i_{\rm L}(t)$ to the input voltage $v_{\rm I}(t)$.

The op amp used in negative feedback, so inputs $e_{-} = e_{+} = 0$. Thus we can label $v_{\rm c} = v_{\rm o}$. KCL at the e_{-} input node is:

$$C\frac{dv_{\rm c}(t)}{dt} + i_{\rm L}(t) + \frac{v_{\rm c}(t)}{R_{\rm F}} + \frac{v_{\rm I}(t)}{R_{\rm I}} = 0$$

and KVL gives:

$$L\frac{di_{\rm L}(t)}{dt} = v_{\rm C}(t)$$

eliminating $v_{\rm c}$ we get:

$$LC\frac{d^{2}i_{\rm L}(t)}{dt^{2}} + \frac{L}{R_{\rm F}}\frac{di_{\rm L}(t)}{dt} + i_{\rm L}(t) + \frac{v_{\rm I}(t)}{R_{\rm I}} = 0$$

or, dividing by LC and moving the source to the right-hand side:

$$\frac{d^2 i_{\rm L}(t)}{dt^2} + \frac{1}{R_{\rm F}C} \frac{d i_{\rm L}(t)}{dt} + \frac{1}{LC} i_{\rm L}(t) = -\frac{1}{LR_{\rm I}C} v_{\rm I}(t)$$

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Part B. Assume that $R_{\rm F} = 1/10 \ \Omega$, $R_{\rm I} = 1/8 \ \Omega$, C = 1 F, and L = 1/9 H. Is this system

- 1. overdamped?
- 2. critically damped?
- 3. underdamped?

Circle your answer and provide an explanation of your choice in the space provided below.

From the differential equation, the characteristic polynomial is:

$$s^2 + \frac{1}{R_{\rm F}C}s + \frac{1}{LC} = 0$$

This is in the standard form $s^2 + 2\alpha s + \omega_0^2 = 0$. So the roots are:

$$s = -\alpha \pm \sqrt{\alpha^2 - \omega_0^2}$$

where $\alpha = \frac{1}{2R_{\rm F}C}$ and $\omega_0^2 = \frac{1}{LC}$.

Plugging in the numbers given we get $\alpha = 5 \text{ s}^{-1}$ and $\omega_0^2 = 9 \text{ s}^{-2}$.

Since $\alpha^2 > \omega_0^2$ the system is **overdamped**, with $s = -5 \pm 4$. So $s_1 = -1$ s⁻¹ and $s_2 = -9$ s⁻¹ 6.2000 Circuits and Electronics

Part C.

Use the device parameters specified in part B. Assume that both the inductor current and the capacitor voltage are zero at time t = 0. At time t = 0 a step of height V_I appears at the input: $v_I(t) = u(t)V_I$. In the box provided below write an expression for the value of the output voltage for all t > 0.

Hint: Compute the inductor current first and then derive the output voltage from your inductor current expression.

For t > 0 $v_{\rm I}(t) = V_{\rm I}$, so a particular solution for the inductor current is $i_{\rm LP}(t) = -V_{\rm I}/R_{\rm I}$, as can be seen from the differential equations.

We need the homogeneous solution to match the initial conditions. This system is overdamped, so we know the homogeneous solution:

$$i_{\rm LH}(t) = A_1 e^{s_1 t} + A_2 e^{s_2 t}$$

for unknown constants A_1 and A_2 , which we will use to match the initial conditions. So the total solution is:

$$i_{\rm L}(t) = -V_{\rm I}/R_{\rm I} + A_1 e^{s_1 t} + A_2 e^{s_2 t} \tag{1}$$

Also $(R_{\rm I} = 1/8 \text{ and } L = 1/9$, but leaving these symbolic here),

$$v_{\rm c}(t) = L \frac{di_{\rm L}(t)}{dt} = L(A_1 s_1 e^{s_1 t} + A_2 s_2 e^{s_2 t})$$
(2)

Since $v_{\rm c}(0+) = 0$ we have $0 = A_1 s_1 + A_2 s_2$ Since $i_{\rm L}(0+) = 0$ we have $0 = -V_{\rm I}/R_{\rm I} + A_1 + A_2$. Thus, $A_1 = -\frac{V_{\rm I}}{R_{\rm I}}\frac{s_2}{s_1-s_2}$ and $A_2 = -\frac{V_{\rm I}}{R_{\rm I}}\frac{s_1}{s_2-s_1}$. In part A we defined $v_{\rm C}$ so that $v_{\rm C} = v_{\rm O}$, so we have the answer as

equation (2):

$$\begin{aligned} v_{\rm o}(t) &= -\frac{LV_{\rm I}}{R_{\rm I}} \frac{s_1 s_2}{s_1 - s_2} e^{s_1 t} - \frac{LV_{\rm I}}{R_{\rm I}} \frac{s_1 s_2}{s_2 - s_1} e^{s_2 t} \\ &= -\frac{LV_{\rm I}}{R_{\rm I}} \frac{(-1)(-9)}{(-1) - (-9)} e^{(-1)t} - \frac{LV_{\rm I}}{R_{\rm I}} \frac{(-1)(-9)}{(-9) - (-1)} e^{(-9)t} \\ &= V_{\rm I}(e^{-9t} - e^{-t}) \end{aligned}$$